Chapter 33 - Electromagnetic Oscillations and Alternating Current

Problem Set #12 - due: Ch 33 - 3, 10, 17, 18, 31, 34, 39, 42, 56, 59, 61, 77, 80, 84, 85, 87

Lecture Outline

- 1. The LC Circuit
- 2. The LRC Circuit
- 3. Basic AC Circuits
- 4. Frequency Filtering Circuits
- 5. The RLC Tuning Circuit
- 6. Power in AC Circuits
- 7. Transformers

We will use the loop and junction theorems to analyze the behavior of circuits that exhibit oscillatory behavior. We will start by allowing the circuits to oscillate at their "natural" frequency, then we will examine the effect of forcing them to oscillate at other frequencies.

<u>1. The LC Circuit</u>



In the circuit at the left, the capacitor is charged when the switch is connected from a to b. Then the switch is connected from b to c. The capacitor begins to lose its charge as it attempts to get current through the inductor. The inductor begins to build up a magnetic field. This field peaks as the charge on the capacitor goes to zero. Then the energy in the inductor begins to create

current to charge the capacitor back up. All of this is explained by the loop theorem which requires that the potential difference across the capacitor always equal the potential difference across the inductor,

$$V_{c} = V_{L} \qquad \frac{Q}{C} = -L\frac{dI}{dt} \qquad \frac{Q}{C} = -L\frac{d^{2}Q}{dt^{2}} \qquad \frac{d^{2}Q}{dt^{2}} = -\frac{1}{LC}Q$$

This differential equation is the SHM equation, $\frac{d^2x}{dt^2} = -\frac{k}{m}x = -\frac{2}{x}x = A\cos(t+)$. By analogy,

$$Q = Q_m \cos(t +)$$
 where $= \frac{1}{\sqrt{LC}}$ The LC Circuit

Example 1: Show that the above expression for the charge on the capacitor satisfies the loop theorem.

$$Q = Q_{m} \cos(t + 1) \quad \frac{dQ}{dt} = -Q_{m} \sin(t + 1) \quad \frac{d^{2}Q}{dt^{2}} = -Q_{m}^{2} \cos(t + 1) = -^{2}Q$$

Substituting into the loop theorem, $-^{2}Q = -\frac{1}{LC}Q = -\frac{1}{\sqrt{LC}}$.

<u>Example 2</u>: For an LC circuit find (a)the peak current in terms of the peak charge on the capacitor and (b)the fraction of the period between the time the capacitor's charge peaks and the current peaks.

(a)From the definition of current, I $\frac{dQ}{dt} = -Q_m \sin(t +)$ $\underline{I_m = Q_m}$. (b)First let's define Q=Q_m when t=0. This establishes the value of , Q = Q_m cos(t +) Q_m = Q_m cos() cos() = 1 = 0 Note that I = I_m when sin(t +) = 1 t + = $\frac{1}{2}$ t = $\frac{1}{2}$ t = $\frac{1}{2}$. Recall, $=\frac{2}{T}$ t = $\frac{2}{2\frac{2}{T}}$ $\frac{t = \frac{1}{4}T}{2}$.

2. The LRC Circuit



The capacitor is charged by connecting a to b. Then the switch is moved from b to c. The oscillations of the LC circuit are damped out by the heat created in the resistor. As usual, the loop theorem gives the details,

$$V_{L} + V_{R} + V_{C} = 0 \qquad L\frac{dI}{dt} + IR + \frac{Q}{C} = 0$$
$$L\frac{d^{2}Q}{dt^{2}} + R\frac{dQ}{dt} + \frac{Q}{C} = 0$$

If we assume that R is fairly small then we would expect damped oscillations of the form, $Q = Q_m e^{-t} \cos_d t$ where Q_m is the initial charge on the capacitor, is the damping constant, and d is the frequency of oscillation. A graph of such a curve is shown at the left. To check to see if this guess solves the differential equation from the loop theorem, we need the derivatives, $\frac{dQ}{dt} = -Q_m e^{-t} \{ \int_d \sin_d t + \cos_d t \}$ and $\frac{d^2Q}{dt^2} = -Q_m e^{-t} \{ \int_d 2 \int_d$

Substituting into the loop theorem,

$$-Q_{m}e^{-t} L\left\{ \begin{pmatrix} 2 & -2 \\ d & -2 \end{pmatrix}\cos dt - 2 & d \sin dt \right\} + R\left\{ d \sin dt + c \cos dt \right\} - \frac{1}{C}\cos dt = 0$$

Sorting things out, $\begin{pmatrix} 2 & -2 \\ d & -2 \end{pmatrix}L + R - \frac{1}{C}\cos dt + \begin{pmatrix} d & R - 2 & dL \\ d & R & -2 & dL \end{pmatrix}\sin dt = 0.$

If this is to be zero for all values of t, then the coefficients of the sine and cosine must be zero independently.

The sine coefficient gives, $_{d}R - 2 _{d}L = 0 = \frac{R}{2L}$.

The cosine coefficient requires,

$$\begin{pmatrix} 2 & -2 \\ d & -2 \end{pmatrix} L + R - \frac{1}{C} = 0 \qquad \begin{pmatrix} 2 & -2 \\ d & -2 \end{pmatrix} + \frac{R}{L} - \frac{1}{LC} = 0 \qquad \begin{pmatrix} 2 & -2 \\ d & -2 \end{pmatrix} + \frac{2}{L} - \frac{1}{LC} = 0.$$
Solving for $d_{t} = \frac{1}{LC} - \frac{2}{L} = \frac{1}{LC} - \frac{R}{2L} \qquad d = \sqrt{\frac{1}{LC} - \frac{R}{2L}^{2}}.$

$$Q = Q_m e^{-\frac{Rt}{2L}} \cos_d t$$
 where $d = \sqrt{\frac{1}{LC} - \frac{R}{2L}^2}$ The LRC Circuit

Example 3: Find the maximum allowed resistance for oscillations to occur. Oscillations will occur as long as d is real, so the critical resistance is when, $\frac{1}{LC} = \frac{R_c}{2L}^2 \qquad R_c = \sqrt{4\frac{L}{C}}.$

Example 4: Find the fraction of energy lost per oscillation for an LRC circuit with small R. At some initial time t the charge is, $Q_0 = Q_m e^{-\frac{R}{2L}t} \cos_d t$. We want to choose this time so that the charge on the capacitor is peaking so, t $n\frac{2}{d}$ where n is an integer. The initial charge is, $Q_0 = Q_m e^{-\frac{R}{2L}n\frac{2}{d}}$. The energy stored in the capacitor at this time is, $U_0 = \frac{1}{2}\frac{Q_0^2}{C} = \frac{1}{2}\frac{Q_m^2}{C}e^{-\frac{R}{L}n\frac{2}{d}}$. After one oscillation, t $(n + 1)\frac{2}{d}$, $Q = Q_m e^{-\frac{R}{2L}(n+1)\frac{2}{d}}$, and $U = \frac{1}{2}\frac{Q_m^2}{C}e^{-\frac{R}{L}(n+1)\frac{2}{d}}$. The fraction of energy lost in one oscillation is, $\frac{U}{U_0} = \frac{U_0 - U}{U_0} = \frac{\frac{1}{2}\frac{Q_m^2}{C}e^{-\frac{R}{L}n\frac{2}{d}} - \frac{1}{2}\frac{Q_m^2}{C}e^{-\frac{R}{L}(n+1)\frac{2}{d}}}{\frac{1}{2}\frac{Q_m^2}{C}e^{-\frac{R}{L}n\frac{2}{d}}} = 1 - e^{-\frac{R}{L}\frac{2}{d}}$. For small R use the Taylor expansion, $e^{-x} = 1 - x + \frac{x^2}{2} - \cdots$ $\frac{U}{U_0}$ $1 - 1 - \frac{2}{dL} = \frac{2}{dL} \frac{R}{dL}$. Now we define the "Q-factor" or "Quality" of the circuit as $\frac{U}{U_0} = \frac{2}{Q} = Q = \frac{dL}{R}$.

<u>3. Basic AC Cicuits</u>

Instead of allowing circuits to oscillate at the frequency they choose, we can force them to oscillate at any frequency we choose by using an AC power supply. The response of the circuit depends on whether it contains resistors, capacitors, or inductors.

Resistors





m I_m The circuit at the left shows a sinusoidally varying power supply connected to a resistor. The resulting current in the circuit is given by Ohm's Rule, $V_R = i_R R$ $i_R = \frac{V_R}{R}$.

By the loop theorem, the voltage on the resistor must always equal the voltage from the power supply so, $i_R = \frac{m \sin t}{R} = \frac{m}{R} \sin t$. The maximum current through the resistor is, $i_{R,max} = \frac{m}{R}$. Note that both the current and the voltage vary with time as the sin t. We say they are "in phase." This is shown in the graph at the left.

The sketch at the left is called a "phasor diagram." Vectors representing the maximum voltage and maximum current rotate counter-clockwise and make at angle t with the positive x-axis. The vertical component of each vector represents the instantaneous value of voltage or current. The ratio of the maximum voltage to the maximum current is defined to be the "impedance."

 $\frac{m}{I_m}$ The Definition of Impedance

For a resistor,

$$R = \frac{m}{\frac{m}{R}} = R.$$

$$R = R$$
The Impedance of a Resistor



The circuit at the left shows a sinusoidally varying power supply connected to a capacitor. The resulting current in the circuit can be found from the definition of capacitance,

$$Q_C = CV_C$$
 and the definition of current $i_C = \frac{dQ_C}{dt}$

By the loop theorem, the voltage on the capacitor must always equal the voltage from the power supply so, $i_{\rm C} = \frac{d}{dt} (CV_{\rm C}) = C \frac{d}{dt} (m \sin t) = C m \cos t.$

The maximum current through the capacitor is,

 $i_{C,max} = C_m$. Note that the current and the voltage are out of phase. The current peaks at an earlier time than the voltage. This is shown in the graph at the left. The phasor diagram for this circuit is shown at the left.

The current leads the voltage by 90° .

The ratio of the maximum voltage to the maximum current is defined to be the impedance,

$$\frac{1}{I_{m}} = \frac{1}{C}$$
 The Impedance of a Capacitor



The circuit at the left shows a sinusoidally varying power supply connected to an inductor. The resulting current in the circuit can be found from the definition of self-inductance $V_L = L \frac{di_L}{dt}$. By the loop theorem, the voltage on the inductor must always equal the voltage from the power supply so,

$$_{n}\sin t = L\frac{di_{L}}{dt}$$
 $i_{L} = -\frac{m}{L}\cos t$. The

maximum current through the inductor is, $i_{L,max} = -\frac{m}{L}$. Note that the current and the voltage are again out of phase. The current peaks at a later time than the voltage. This is shown in the graph at the left.

Physics 4B Lecture Notes



The phasor diagram for this circuit is also shown at the left. The current lags the voltage by 90° . The ratio of the maximum voltage to the maximum

current is defined to be the impedance, $\frac{m}{I_m}$ $L = \frac{m}{\frac{m}{I}} = L$.

L = L The Impedance of an Inductor

Now that we see that the impedance of inductors and capacitors depends on the frequency of the power source we can begin to design circuits that have a frequency dependence such as crossovers and tuners. The concept of phasor diagrams cam be extended to more complex circuits.

<u>4. Frequency Filtering Circuits</u>

 $_{\rm L} = {\rm L}$ Inductors stop high frequency and pass low frequency. $_{\rm C} = \frac{1}{{\rm C}}$ Capacitors stop low frequency and pass high frequency. $_{\rm R} = {\rm R}$ Resistors don't care about frequency.

<u>Example 5:</u> A stereo signal contains frequencies from 20.0Hz to 20.0kHz. A tweeter has a resistance of 8.00 and functions well between 5.00kHz and 20.0kHz. (a)Design a circuit which will use an inductor to filter out the low frequencies. (b)Find the inductance so that it has the same peak current as the tweeter at 5.00kHz. (c)Find the ratio of the peak currents at 10.0kHz, (d)at 1.00kHz. (e)Include the woofer in the circuit.

(a)The power supply will send a variety of frequencies toward the speaker. Putting an inductor across the speaker allows the low frequencies to bypass the tweeter.



(b)The voltage across the inductor must always equal the voltage

across the tweeter according to the loop theorem so, $m = i_{L,max}$ $L = i_{R,max}$ R.

At 5.00kHz the peak current is the same in both the resistor and the inductor so, $i_{L,max} = i_{R,max}$

which means that
$$_{L} = _{R}$$
 $L = R$ $L = \frac{R}{2} = \frac{R}{2} = \frac{8.00}{2} = \frac{0.255 \text{mH}}{2.5500} = \frac{0.255 \text{mH}}{2}$.

(c)It's still true that

Physics 4B Lecture Notes

(e)The woofer should go in series with the inductor since the low frequencies prefer this leg of the circuit. Usually a capacitor is put in the leg with the tweeter to adjust the frequency that produces equal currents. This type of circuit is called a "crossover."



<u>5. The RLC Tuning Circuit</u>



In the RLC series circuit, the inductor stops the high frequencies and the capacitor stops the low frequencies. Only frequencies in between can make it through the circuit. Let's find out the relationship between the peak current and the frequency. The junction theorem requires the current at any time to be equal in all the circuit elements, $i(t) = i_R(t) = i_L(t) = i_C(t)$. The loop theorem demands that the sum of the voltage drops at any time add up to the supply voltage, $(t) = V_R(t) + V_L(t) + V_C(t)$.



Using the definition of impedance,

Since the voltages are out of phase we need to add up the phasors. According to the phasor diagram,

$$_{\rm m} = \sqrt{V_{\rm R,max}^2 + (V_{\rm L,max} - V_{\rm C,max})^2}$$

Using the definition of impedance,

$$_{\rm m} = \sqrt{(i_{\rm max}R)^2 + (i_{\rm max} L - i_{\rm max} C)^2}.$$

Factoring out the peak current,

$$m = i_{max} \sqrt{R^2 + \begin{pmatrix} L - C \end{pmatrix}^2}.$$

$$\frac{m}{n} = \sqrt{R^2 + \begin{pmatrix} L - C \end{pmatrix}^2}.$$

Usually, the symbol Z is used for the total impedance of a circuit.

$$Z = \sqrt{R^2 + (L - C)^2}$$
 RLC Series Circuit: Impedance

The phase angle between the voltage and current can be found from the definition of tangent,

$$\tan = \frac{V_{L,max} - V_{C,max}}{V_{R,max}} = \frac{i_{max} - i_{max} - c}{i_{max}R} = \frac{L - c}{R}$$
$$\tan = \frac{L - c}{R} \quad \text{RLC Series Circuit: Phase Angle}$$

Example 6: An RLC series circuit contains a 20.0 resistor, a 180mH inductor and a 60.0µF capacitor connected to a 110V-rms 60.0Hz power supply. Find the rms current and voltage for each circuit element.

Since all the circuit elements are in series they have the same current. To find this current we need the total impedance, $Z = \sqrt{R^2 + (L - C)^2}$. The impedance for each element is, R=20.0, $\begin{array}{ll} L = & L = 2 & fL = 2 & (60.0)(0.180) = 67.9 & \text{and} \\ C = & \frac{1}{-C} = \frac{1}{2 & fC} = \frac{1}{2 & (60.0)(60.0 \times 10^{-6})} = 44.2 & . \end{array}$

The total impedance is, $Z = \sqrt{(20.0)^2 + (67.9 - 44.2)^2} = 31.0$

The current can be found by the definition of impedance,

$$\frac{m}{I_{m}} = \frac{\sqrt[m]{\sqrt{2}}}{\frac{I_{m}}{\sqrt{2}}} = \frac{rms}{I_{rms}} \qquad I_{rms} = \frac{rms}{Z} = \frac{110}{31.0} = \underline{3.55A}.$$

The rms voltage on each one can be found from their impedances: $V_{R,rms} = i_{rms}R = \underline{\underline{71.0V}}, V_{L,rms} = i_{rms} L = \underline{\underline{241V}} \text{ and } V_{C,rms} = i_{rms} C = \underline{\underline{157V}}.$

Why don't these add up to the supply voltage? These voltages don't happen at the same time. How can they be greater than the supply voltage? The other voltages are negative at these times.



The current that passes through the circuit depends on the frequency. This is called the "frequency response."

$$_{m} = i_{max}\sqrt{R^{2} + (L - C)^{2}}$$
 $i_{max} = \frac{m}{\sqrt{R^{2} + (L - \frac{1}{C})^{2}}}$

The peak current is plotted against the frequency at the left. Note that the capacitor cuts off the low frequencies and the inductor cuts off the high frequencies. The frequency for maximum current is called the "resonance frequency."

This occurs when
$$L - \frac{1}{C} = 0$$
 $_{o} = \frac{1}{\sqrt{LC}}$

RLC Series Circuit: Resonance Frequency

The fact that only a small range of frequencies make it

through this circuit make it ideal for tuning as in a radio. A typical tuning circuit is shown at the left. The tuning is usually accomplished by varying the capacitance.

Example 7: Find the full width of the resonance curve when the peak current is half its maximum value.

$$i_{\text{max}} = \frac{1}{2}i_{\text{m,max}}$$

Using the definition of impedance, the total impedance of the RLC circuit and the fact that the term in parentheses is zero at resonance,

$$\frac{\mathrm{m}}{\sqrt{\mathrm{R}^{2} + \left(\mathrm{L} - \frac{\mathrm{l}}{\mathrm{C}}\right)^{2}}} = \frac{1}{2} \frac{\mathrm{m}}{\sqrt{\mathrm{R}^{2} + \left(\mathrm{L} - \frac{\mathrm{l}}{\mathrm{o}^{\mathrm{C}}}\right)^{2}}} \qquad \frac{\mathrm{m}}{\sqrt{\mathrm{R}^{2} + \left(\mathrm{L} - \frac{\mathrm{l}}{\mathrm{C}}\right)^{2}}} = \frac{\mathrm{m}}{2\mathrm{R}}$$

$$\sqrt{R^{2} + \left(L - \frac{1}{C}\right)^{2}} = 4R^{2} + \left(L - \frac{1}{C}\right)^{2} = 3R^{2} + \left(L - \frac{1}{C}\right)^{2} = \sqrt{3}R.$$
Canceling the driving voltage and squaring both sides,

$$R^{2} + \left(L - \frac{1}{C}\right)^{2} = 4R^{2} + \left(L - \frac{1}{C}\right)^{2} = 3R^{2} + \left(L - \frac{1}{C}\right)^{2} = 3R^{2} + \left(L - \frac{1}{C}\right)^{2} = \sqrt{3}R.$$

Assume the frequencies we want, , are symmetrically located about the resonance frequency,

$$= {}_{0} \pm \frac{1}{2} = {}_{0} 1 \pm \frac{1}{2} {}_{0} \qquad \frac{1}{2} = \frac{1}{0} 1 \pm \frac{1}{2} {}_{0} \qquad \frac{1}{0} 1 \pm \frac{1}{2} {}_{0}$$

Substituting back into the previous equation,

С

$$1 \pm \frac{1}{2_{0}} = \frac{1}{0} L - 1 \mp \frac{1}{0} = \sqrt{3}R \qquad \left(\frac{1}{0} L - \frac{1}{0}C \right) \pm \frac{1}{2_{0}} \left(\frac{1}{0} L + \frac{1}{0}C \right) = \sqrt{3}R$$

Since $= \frac{1}{\sqrt{LC}} = \frac{1}{0}L = \frac{1}{0}C$, we get, $0 \pm \frac{1}{2_{0}} \left(2_{0}L \right) = \sqrt{3}R = \pm \frac{\sqrt{3}R}{L}$.

The width of the curve only depends on R and L not on C. Radios use capacitors to tune so that the stations can be equally spaced.

$$= \frac{\sqrt{3}R}{L}$$
 RLC Series Circuit: Full Width Half Maximum

Example 8: Design an RLC series circuit that is an FM tuner. The frequency of a typical FM station is 100MHz. The spacing between stations is 0.200MHz. Assume the circuit requires a 5.00mA peak current and the antenna induces a 100mV emf.

The resistance can be found from the relationship between the peak current and peak voltage,

$$i_{max} = \frac{m}{\sqrt{R^2 + (L - \frac{1}{C})^2}}$$
 $i_{m,max} = \frac{m}{R}$ $R = \frac{m}{i_{m,max}} = \frac{100mV}{5.00mA} = \frac{20.0}{5.00mA}$

The inductance is fixed by the spacing between stations,

$$= \frac{\sqrt{3R}}{L} \qquad L = \frac{\sqrt{3R}}{2} = \frac{\sqrt{3}(20.0)}{2(0.200 \times 10^6)} = \frac{27.6 \mu H}{2}.$$

The capacitance is used to tune for the desired frequency,



6. Power in AC Circuits



Energy supplied to capacitors is stored in the electric field between the plates. This energy will be returned in full when the capacitor discharges. Energy supplied to inductors is stored in the magnetic field of the coils. This energy is also returned in full when the field collapses. The only place where energy is "lost" to the circuit is in the resistor where heat is produced and not returned. The power consumed by a resistor is $P = iV_R$ as before. The only difference in an AC circuit is that the current and voltage oscillate with time,

 $P(t) = (i_{max} \sin t)(V_{R,max} \sin t) = i_{max} V_{R,max} \sin^2 t$. So the peak power consumed is, $P = i_{max}V_{R,max}$. In terms of the peak voltage supplied to the circuit, $P = i_{max} cos$. The cos is called the "power factor." Using Ohm's Rule, $P = i_{max}V_{R,max} = i_{max}(i_{max}R) = i_{max}^2R$ as you might suspect. The average power consumed is, $P_{av} = \overline{i^2}R = i_{rms}^2R$.

 $P_{av} = i_{rms}^2 R$ Average Power Consumed

<u>Example 9:</u> For the circuit of example 2, find the average power delivered (a)at 60Hz and (b)at the resonance frequency.

(a) The average power delivered will match the average power consumed. Using the numbers from example 2

$$P_{av} = i_{rms}^2 R = (3.55)^2 (20.0) = \underline{252W}$$

(b)At resonance, the impedance is equal to the resistance, so the rms current is,

$$I_{\rm rms} = \frac{\rm rms}{\rm Z} = \frac{\rm rms}{\rm R} = \frac{110}{20.0} = 5.50 {\rm A}$$

The average power consumed will be, $P_{av} = i_{rms}^2 R = (5.50)^2 (20.0) = \underline{605W}$.

Since the current is a maximum at resonance, the power consumed also is maximum.

7. Transformers



Transformers are used to convert current to voltage or vice-versa. Assuming no losses to heat the Law of Conservation of Energy requires that the power that comes in must equal the power that goes out, $I_pV_p = I_sV_s$. The changing current in the primary coils creates a changing magnetic field in the iron. This changing magnetic field through the secondary coils creates an induced voltage according to Faraday's Law.

Assuming that the iron keeps the flux the same in both coils, $p = \frac{d}{dt} = \frac{d}{dt} = \frac{d}{dt}$.

Using Faraday's Law, $\frac{d_p}{dt} = \frac{d_s}{dt}$ $\frac{V_p}{N_p} = \frac{V_s}{N_s}$. In summary,

$I_p V_p = I_s V_s$	Transformers: Conservation of Energy
$\frac{V_p}{N_p} = \frac{V_s}{N_s}$	Equal Flux

If the voltage increases we call it a "step-up" transformer, if it decreases we call it a "step-down" transformer.

Example 10: Design a transformer to convert standard 110V to 12V for a laptop power supply. The secondary side has 100 turns and delivers 100mA. Find the number of turns needed in the primary and the current the primary will draw.

The requirement of equal flux through the primary and secondary yields,

$$\frac{\mathbf{V}_{p}}{\mathbf{N}_{p}} = \frac{\mathbf{V}_{s}}{\mathbf{N}_{s}} \qquad \mathbf{N}_{p} = \mathbf{N}_{s} \frac{\mathbf{V}_{p}}{\mathbf{V}_{s}} = 100 \frac{110}{12} = \underline{917 \text{ turns}}.$$

The Law of Conservation of Energy requires,

$$I_p V_p = I_s V_s$$
 $I_p = I_s \frac{V_s}{V_p} = (100) \frac{12}{110} = \underline{10.9 \text{mA}}.$

Chapter 33 - Summary

The LC Circuit Q = Q_m cos(t +) where = $\frac{1}{\sqrt{LC}}$ The LRC Circuit Q = Q_me^{$-\frac{Rt}{2L}$} cos_dt where $d = \sqrt{\frac{1}{LC} - \frac{R}{2L}^2}$ The Definition of Impedance $\frac{m}{I_m}$ The Impedance of a Resistor R = RThe Impedance of a Capacitor $C = \frac{1}{C}$ The Impedance of an Inductor L = LRLC Series Circuit: Impedance Z = $\sqrt{R^2 + (L - C)^2}$ Phase Angle tan $= \frac{L^- C}{R}$ Resonance Frequency $_0 = \frac{1}{\sqrt{LC}}$ Full Width Half Maximum $= \frac{\sqrt{3}R}{L}$ Average Power Consumed $P_{av} = i_{rms}^2 R$ Transformers Conservation of Energy $I_p V_p = I_s V_s$ Equal Flux $\frac{V_p}{N_p} = \frac{V_s}{N_s}$